

PRINTER RUSH

(PTO ASSISTANCE)

Application : 10/688, 731

Examiner : Le

GAU : 2818

From : DP

Location : IDC FMF FDC

Date : 3-21-06

Tracking #: epm 10/688, 731 Week Date: 2-6-2006

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input type="checkbox"/> DRW		
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input checked="" type="checkbox"/> SPEC	<u>10-15-2003</u>	

[RUSH] MESSAGE: please provide U.S. Application number
(Docket No. 10019410-1) on page 9 line 15 of Specification.

Thank you.

[XRUSH] RESPONSE: _____

see misc comm

INITIALS: Le

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Z. Chen et al.

Art Unit: 2818

To: Rori Burch

Serial Number: 10/688,731

Publishing Division

Filed: October 15, 2003

Title: April 5, 2006

Dear Rori,

In response to the "Notice to File Corrected Application Papers" dated March 30, 2006, please correct the paragraph starting on Page 9, line 15 as follows: (please note that the cited application has been issued as a US Patent).

In optional step 504, an emission layer 56 (such as an anisotropic conductivity layer or nodule layer) is formed over the surface of the substrate (see Fig. 7C) and isolation layer 58 to substantially uniformly distribute electrons from the substrate to latter defined emitter surfaces. The emission layer 56 is preferably formed using polysilicon with a thickness of about 0.1 to 2 microns. The polysilicon can be optionally anodized or non-anodized. An exemplary nodule layer is described and shown in commonly assigned US patent application Serial No. 09/975296 filed October 12, 2001, herein incorporated by reference. An exemplary anisotropic conductivity layer is described and shown in commonly assigned US patent application Serial No. 10/066158 ~~attorney docket No. 10019410-1~~ filed January 31, 2002, now U.S. Patent No. 6,835,947 ~~concurrent with this application~~, herein incorporated by reference.

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Respectfully Submitted,

Z. Chen et al.

By: 

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UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Serial Number
10688731

Date Mailed
3/30/06

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Notice of Allowance Mailed

This application has been accorded an Allowance Date and is being prepared for issuance. The application, however, is incomplete for the reasons below.

Applicant is given 30 days from the mail date of this Notice within which to correct the informalities indicated below. A failure to reply will result in the application being ABANDONED. This period for reply is NOT extendable under 37 CFR 1.136 (a) or (b).

- ♦ Specification page 9, line 15 serial number is missing. Fax missing information to number below or e-mail PDF.
- For status updates visit <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR System, contact the Electronic Business Center (EBC) toll free at 866-217-9197.

APPLICANT MUST SUPPLY MISSING INFORMATION WITHIN 30 DAYS OF THE MAIL DATE OF THIS NOTICE.

A copy of this notice **MUST** be returned with the reply.

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